

U.S. Patent Application Serial No. 10/612,990
Supplemental Response filed October 18, 2005
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AMENDMENTS TO THE CLAIMS:

Claim 1 has been amended as follows. This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Currently Amended): A multilayer circuit board, comprising:

a plurality of cable layers, each of which includes electric conductive sections;

a plurality of first insulating layers, each of which encloses said electric conductive sections in each cable layer and fills spaces between said electric conductive sections, each of said electric conductive sections of each of said cable layers including a cable pattern and a land pad;

post vias electrically connecting said electric conductive sections in one cable layer to those in another cable layer,

wherein height of said electric conductive sections in each cable layer are regulated by abrading to be equal to that of said first insulating layer enclosing those electric conductive sections;
and

a second insulating layer, which is formed to enclose said post vias,

wherein height of said post vias are regulated by abrading to be equal to that of said second insulating layer.